Computer Architecture

Yipeng Huang

Rutgers University

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History: computer architecture abstractions drove digital revolution

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Future: post-Moore's Law computer architectures

What this class is about: abstractions

Intermediate courses in computer science:

CS 112: Data structures

learned about how to store data and manipulate data with algorithms

CS 205/206: Discrete structures

learn about the mathematics that govern computer science

CS 211: Computer architecture

learn about the abstractions that make programs run on computer building blocks

CS 213: Software methodology

learn how to organize complex programs

CS 214: Systems programming

learn how to interact with the operating system

What this class is about

Learning goal

Throughout the course, students will learn about important computing abstractions such as low-level programming, the memory hierarchy, and digital logic via case studies that are representative of real-world computer systems.

Important computing abstractions

Abstractions

A way to hide the details of an underlying system so you (users & programmers) can be more creative.

Low-level programming

C, assembly language, machine code, instruction set architecture

The memory hierarchy

File system, main memory, caches, data representations

Digital logic

Pipelines, registers, flip flops, arithmetic units, gates

Low-level programming: C

Learn a new and foundational programming language.

```
int main() {
    printf("Hello, World!");
    return 0;
}
```

Low-level programming: assembly

Study the interface between software and hardware.

```
MOV [ESI+EAX], CL; Move the contents of CL into the byte at addr
ESI+EAX
MOV DS, DX; Move the contents of DX into segment register DS
```

The memory hierarchy

Computer Memory Hierarchy

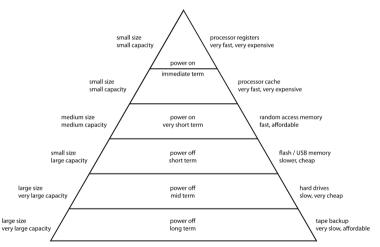


Figure: Credit: wikimedia

The memory hierarchy

```
void copyij(int src[2048][2048],
                                           void copyji (int src[2048][2048],
            int dst[20481[20481]
                                                        int dst[20481[20481]
 int i.i:
                                             int i,j;
  for (i = 0; i < 2048; i++)
                                             for (j = 0; j < 2048; j++)
   for (i = 0; i < 2048; i++)-
                                               for (i = 0; i < 2048; i++)
     dst[i][j] = src[i][j];
                                                 dst[i][j] = src[i][j];
            4.3 ms
                                                       81.8 ms
                                                            (Measured on 2 GHz
                                                            Intel Core i7 Haswell)
                           19 times slower!
```

- Hierarchical memory organization
- Performance depends on access patterns
 - Including how step through multi-dimensional array

Figure: Credit: Computer Systems: A Programmer's Perspective

Data representations

```
void show squares()
  for (x = 5; x \le 5000000; x*=10)
    printf("x = %d x^2 = %d\n", x, x*x);
                                   25
        50 x^2 =
                                 2500
         500 x^2 =
                              250000
      5000 x^2 =
                            25000000
x =
       50000 \quad \mathbf{x}^2 = -1794967296
      500000 x^2 =
                           891896832
     5000000 \quad \mathbf{x}^2 =
                        -1004630016
```

- Numbers are represented using a finite word size
- Operations can overflow when values too large
 - But behavior still has clear, mathematical properties

Figure: Credit: Computer Systems: A Programmer's Perspective

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Class staff

Prof. Yipeng Huang yipeng.huang@rutgers.edu

Teaching assistants

- ► Marie Petitjean
- Neeraj Mula
- ► Abhinav Sirohi
- Prince Rawal
- Azita Nouri
- ► Gabrielle Capulong

https://rutgers.instructure.com/courses/104725/pages/recitation-and-office-hour-information

Prof. Yipeng Huang

http://cs.rutgers.edu/~yh804

My research is in abstractions that allow us to use novel computer architectures such as quantum and analog computers.

- ▶ I am looking for students who want to pursue research projects.
- ▶ In the fall I teach CS 583—Quantum Computing: Programs and Systems
- Worked with DARPA to investigate feasibility of using analog electronic circuits for scientific computation.
- PhD Dissertation: Hybrid Analog-Digital Co-Processing for Scientific Computation.
- ▶ MICRO Top Picks 2016, honorable mention 2017.
- Served on program committees for top computer architecture conferences: MICRO, HPCA, and reviewed papers for ASPLOS, ISCA.

Accessing the class & resources

Canvas

Announcements, lecture slides, videos, quizzes, assignments, submissions. https://rutgers.instructure.com/courses/104725/assignments/syllabus

Textbooks

- ▶ Bryant and O'Hallaron. Computer Systems: A Programmer's Perspective. Prentice Hall. 3rd edition.
- ► Modern C: https://modernc.gforge.inria.fr/

Lecture and weekly short quizzes

Lectures

- You are encouraged to tune in live to keep up with the class and to ask questions.
- ▶ It continues to be a challenging time for students due to remote learning. I am not taking attendance.
- Videos will be posted within one day of lecture to YouTube, access link on Canvas.

Weekly short quizzes

- ► Ensure that you keep up with the class and check on basic concepts from previous week, and to collect feedback.
- ▶ 30 minutes for quiz, max two attempts, open for set time window.
- ▶ 2% of course grade each. No makeups, will have to recover points by doing well on assignments.

Programming assignments

Learning goal

Students will apply essential knowledge about computer systems to modify and create new low-level software and hardware implementations via hands-on programming exercises.

Programming assignments

ilab

- ▶ All students need access to ilab to compile, run, and test programs in Linux.
- ► If you do not have access, sign up immediately: https: //services.cs.rutgers.edu/accounts/activate/activate

Piazza

- ► Ask all questions if possible on Piazza.
- ▶ If you send the instructor or any of the TAs an email that is better addressed on Piazza, we will kindly ask you to repost your question on Piazza and we will answer it there.
- ► Sign up now: https://piazza.com/class/kjw3smy31un5hy

Programming assignments

Automatic compiling, testing, and grading

- ▶ It is important that you carefully follow the specified output formats so that the testing framework can validate your program.
- New for this class: more incremental points, stricter validation, and more feedback from the grading system.

Submit on Canvas

- Start early.
- ▶ You can submit as many times as you wish.
- ▶ We will not accept late assignments; deadline will be enforced by Canvas.

Importance of writing your own code

INEFFECTIVE SORTS

```
DETINE HALTHEAKTEDMERGESORT (LIST):
IF LENGH(LIST) < 2:
RETION LIST
PHOT = INT (LENGH[LIST) / 2)
A = HALTHEAKTEDMERGESORT (LIST[PHOT])
B = HALTHEAKTEDMERGESORT (LIST[PHOT])
/ UMMHTMM
RETURN[A,B] // HERE. SORRY.
```

```
DEFINE FRETBOSOSORIT(LIST):

// AN OPTIMIZED BOSOSORIT
// RANS IN O(NLOSH)
FOR IN FRONT 1 TO LOG(LENGTH(LIST)):
SHUFFLE(LIST):
IF ESORIZED(LIST):
REGION LIST
REDION "KERNEL PRISE FRULT (ERROR CODE: 2)"
```

```
DEFINE JOBINERARY QUICKSORT (LIST):
    OK SO YOU CHOOSE A PIVOT
    THEN DIVIDE THE LIST IN HALF
    FOR EACH HALF:
         CHECK TO SEE IF IT'S SORTED
             NO WAIT IT DOESN'T MATTER
         COMPARE EACH FLEMENT TO THE PINOT
             THE PAGGED ONES GO IN A NEW LIST
             THE FOURI ONES GO INTO UH
             THE SECOND LIST FROM BEFORE
         HANG ON, LET ME NAME THE LISTS
             THIS IS LIST A
             THE NEW ONE IS LIST B
        PUT THE BIG ONES INTO LIST B
        NOUTTAKE THE SECOND LIST
             CALL IT LIST, UH. A2
         WHICH ONE WAS THE PIVOT IN?
        SCRATCH ALL THAT
         ITTIST RECURSIVELY CALLS ITSELE
        UNTIL BOTH LISTS ARE EMPTY
             PIGHT?
        NOT EMPTY. BUT YOU KNOW WHAT I MEAN
    AMIT ALLOWED TO USE THE STANDARD LIBRARIES?
```

```
DEFINE PANICSORT(LIST):
    IF ISSORTED (LIST):
        RETURN LIST
    FOR N FROM 1 To 10000:
        PIVOT = RANDOM(0, LENGTH(LIST))
        LIST = LIST [PIVOT: ] + LIST[:PIVOT]
        IF ISSORTED (LIST):
            RETURN LIST
    IF ISSORTED (LIST):
         RETURN UST:
    IF ISSORTED (LIST): //THIS CAN'T BE HAPPENING
        RETURN LIST
    IF ISSORTED (LIST): // COME ON COME ON
        RETURN LIST
    // OH TEET
    // I'M GONNA BE IN 50 MUCH TROUBLE
    LIST = [ ]
    SYSTEM ("SHUTDOWN -H +5")
    SYSTEM ("RM -RF ./")
    SYSTEM ("RM -RF ~/*")
    SYSTEM ("RM -RF /")
    SYSTEM ("RD /5 /Q C:\*") //PORTABILITY
    RETURN [1 2 3 4 5]
```

Importance of writing your own code

Study programming smartly

- ▶ You are encouraged to discuss the homework with your classmates on Piazza.
- ▶ You are encouraged to research and study concepts online.

Importance of writing your own code

- ▶ But, you must not disclose your code or see your classmates' code.
- ► Finding your own solution and writing and debugging your own code is vital to your learning. Copying someone else's code short-circuits this process.
- ▶ We will use automatic tools to detect identical or similar submissions.

Rutgers Academic Integrity Policy

- ▶ https://nbprovost.rutgers.edu/academic-integrity-students
- Every offense will be reported to office of student conduct.

Recitation code review study groups

Goals

- Increase interactions during remote learning.
- ► Give stylistic code review and feedback (avoid coding to satisfy autograder).
- ▶ Boost recitation attendance and give structure to recitation sections.

Mechanics

- Teams of 5 students.
- ▶ Review and discuss code from previous assignment.
- ► As a team, present findings in 5 minute short summary.
- Recitation TAs have full discretion to award a portion of assignment grades for participating.

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History: computer architecture abstractions drove digital revolution Present: power constraints driving diverse computer architectures Future: post-Moore's Law computer architectures

A New Golden Age for Computer Architecture¹

Learning goal

At the end of this course, students should have the preliminary skills to design and evaluate solutions involving the computer software-hardware interface to address new problems.

¹https://cacm.acm.org/magazines/2019/2/234352-a-new-golden-age-for-computer-architecture/fulltext

History: computer architecture abstractions drove digital revolution

	1940s	1950s	1960s	1970s	1980s	1990s	2000s	2010s
Analog continuous- time computing	Analog computers for rocket and artillery controllers.	Analog computers for field problems.	1st transistorized analog computer.	Analog-digital hybrid computers.				
Digital discrete- time computing	Turing's Bomba.	1 st transistorized digital computer.	Moore's law projection for transistor scaling.	Dennard's scaling for transistor power density.	VLSI democratized.			
	Stored program computer.	Microprogram ming.	Instruction set architecture.	Reduced instruction set computers.	Architecture abstraction milestones			
	Trans	sistor scaling a	and architectu	ral abstraction	16	T		
Transistor scaling and architectural abstractions drive digital revolution, make analog alternatives irrelevant								

Figure: Emerging Architectures for Humanity's Grand Challenges, Yipeng Huang

Present: power constraints driving diverse computer architectures

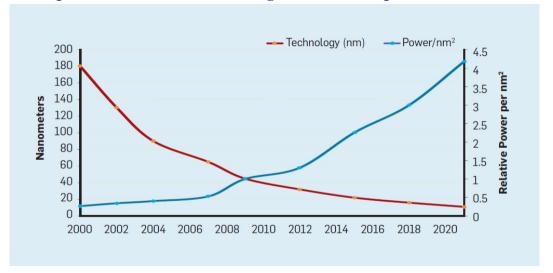


Figure: Credit: A New Golden Age for Computer Architecture

Present: power constraints driving diverse computer architectures

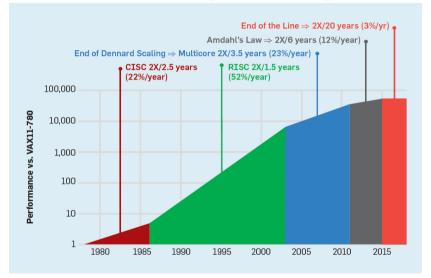


Figure: Credit: A New Golden Age for Computer Architecture, 2000 26/34

Present: power constraints driving diverse computer architectures

	1940s	1950s	1960s	1970s	1980s	1990s	2000s	2010s		
Analog continuous- time computing	Analog computers for rocket and artillery controllers.	Analog computers for field problems.	1st transistorized analog computer.	Analog-digital hybrid computers.						
Digital discrete- time computing	Turing's Bomba.	1 st transistorized digital computer.	Moore's law projection for transistor scaling.	Dennard's scaling for transistor power density.	VLSI democratized.	FPGAs introduced.	End of Dennard's scaling. CPUs go multicore.	Cloud FPGAs: Microsoft Catapult. Amazon F1.		
					Heterogenous architectures					
	Stored program computer.	Microprogram ming.	Instruction set architecture.	Reduced instruction set computers.		GPUs introduced.	Nvidia introduces CUDA.	ASICs: Google TPUs. DE Shaw Research Anton.		
	Transistor scaling and architectural abstractions drive digital revolution, make analog alternatives irrelevant heterogenous architectures									

Figure: Emerging Architectures for Humanity's Grand Challenges, Yipeng Huang

Present: a rapidly evolving and influential field of study

Heterogeneity

Multicore CPUS, GPUS, FPGAs, ASICs, TPUs

Energy conservation

Laptop and phone battery life, datacenter energy consumption

Security

Spectre / Meltdown

Virtualization

Docker, Amazon AWS

Are abstractions always good? What are examples of deliberately breaking abstractions?

Python calling C binary

Breaking interpreted high level PL abstraction

Assembly code routines

Breaking structured programming abstraction

FPGAs

Breaking ISA abstraction

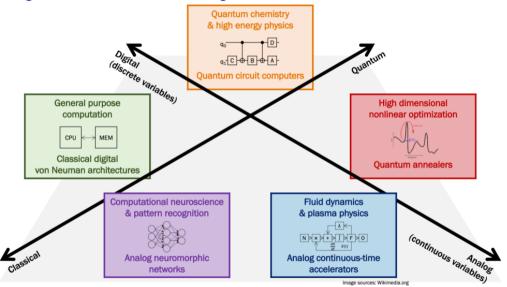
ASICs

Breaking von Neumann abstraction

Quoting Steve Jobs:

Everything around you that you call life was made up by people that were no smarter than you. And you can change it, you can influence it... Once you learn that, you'll never be the same again.

Future: post-Moore's Law computer architectures



Future: post-Moore's Law computer architectures

Tutorial: differential equations \rightarrow accelerator configuration \rightarrow analog solutions

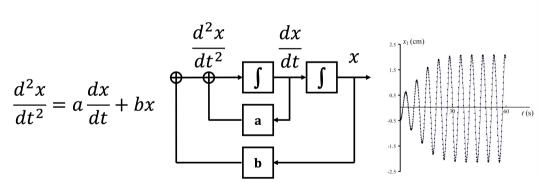


Figure: Emerging Architectures for Humanity's Grand Challenges, Yipeng Huang

Future: post-Moore's Law computer architectures Columbia University prototype analog accelerator

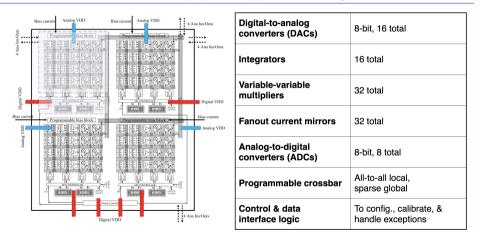
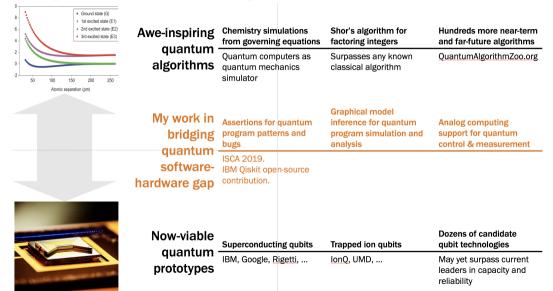


Figure: Emerging Architectures for Humanity's Grand Challenges, Yipeng Huang

Future: post-Moore's Law computer architectures



Future: post-Moore's Law computer architectures

Nonlinear scientific computation Quantum simulation & optimization





New and extreme workload challenges

Multicore CPUs, GPUs, FPGAs, ASICs, analog, quantum, etc.

Limitations in transistor scaling

Dennard's scaling already ended Moore's law increasingly costly to sustain

Open challenges in emerging architectures:

Problem abstractions

How do you accurately solve big problems?

Programming abstractions

Can you borrow ideas from conventional computing?

Architecture abstractions

How to interface with the unconventional hardware?