

Assembly: Arithmetic operations and control flow.

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MOV instruction sign extension

Arithmetic instructions

- Shift operations

- Bitwise operations

- Integer arithmetic operations

- Load effective address

Control flow

Looking ahead

Class plan

1. Thursday, 3/4: Assembly arithmetic operations and control flow.
2. Code review session for PA2 is the week of 3/8 - 3/12. TAs will take attendance to assign participation points.
3. Reading assignment for next three weeks: CS:APP Chapter 3.
4. Programming Assignment 3 on bits, bytes, integers, floats out. Due Monday March 22.

Programming Assignment 3: binSub

- ▶ PA3 is structured in terms of difficulty similarly to PA1 and PA2.
- ▶ The assignment rewards you for starting early.
- ▶ Use Piazza; We rely on it to gauge what needs further explanation.
- ▶ Later parts (parts 4 and 5) are more open-ended.

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Sign extension due to unsigned and signed data types

Converting to a data type with more bits

```
1 unsigned short uc_to_us (  
2     unsigned char input  
3 ) {  
4     return input;  
5 }
```

```
1 signed short sc_to_ss (  
2     signed char input  
3 ) {  
4     return input;  
5 }
```

$$\begin{aligned}255 &= 1111_1111_2 \\ &= 0000_0000_1111_1111_2 \\ &= 255\end{aligned}$$

$$\begin{aligned}127 &= 0111_1111_2 \\ &= 0000_0000_0111_1111_2 \\ &= 127\end{aligned}$$

$$\begin{aligned}-128 &= 1000_0000_2 \\ &= 1111_1111_1000_0000_2 \\ &= -128\end{aligned}$$

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Bitwise operations

Assembly instruction	Instruction effect
<code>notq dest</code>	$\sim \text{dest} \rightarrow \text{dest}$
<code>andq src, dest</code>	$\text{src} \& \text{dest} \rightarrow \text{dest}$
<code>orq src, dest</code>	$\text{src} \text{dest} \rightarrow \text{dest}$
<code>xorq src, dest</code>	$\text{src} \wedge \text{dest} \rightarrow \text{dest}$

Integer arithmetic operations

Assembly instruction	Instruction effect
<code>incq dest</code>	$\text{dest} + 1 \rightarrow \text{dest}$
<code>decq dest</code>	$\text{dest} - 1 \rightarrow \text{dest}$
<code>negq dest</code>	$-\text{dest} \rightarrow \text{dest}$
<code>addq src, dest</code>	$\text{src} + \text{dest} \rightarrow \text{dest}$
<code>subq src, dest</code>	$\text{src} - \text{dest} \rightarrow \text{dest}$
<code>imulq src, dest</code>	$\text{src} \times \text{dest} \rightarrow \text{dest}$

Load effective address

```
1 long * leaq (  
2     long * ptr, long index  
3 ) {  
4     return &ptr[index+1];  
5 }
```

```
1 long mulAdd (  
2     long base, long index  
3 ) {  
4     return base+index*8+8;  
5 }
```

Both C code functions above translate to the assembly on the right.

leaq:

mulAdd:

```
leaq 8(%rdi,%rsi,8), %rax  
ret
```

Explanation

- ▶ `leaq src, dest` takes the effective address of the memory (index, displacement) expression of `src` and puts it in `dest`.
- ▶ `leaq` has shorter latency (takes fewer CPU cycles) than `imulq`, so GCC will use `leaq` whenever it can to calculate expressions like $y + ax + b$.

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